LINE#	SOURCE TEXT
1	TH = a random number between VMIN and VMAX
2	FOR V from VMIN to VMAX do
3	IF V < TH THEN
4	DO statements1 {branch1}
5	ELSE
6	DO statements2 {branch2}
7	OF

<u>10</u>

Figure 1

Line #	Source Text	Comment
A1	MOV A,V	Block a
A2	CALL IRRITATE_1	
A3	JMP DO_REAL	
A4	JMP DO_VOID	
	...	
	...	
B1	IRRITATE_1: MOV R0,KNOWN_DISPLACEMENT	Block b
B2	SUB R0,THRESHOLD	
b3	MOV DPTR,R0	
b4	JMP @A+DTPR	
	...	
	...	
c1	JMP L1	Block c
c2	JMP L1	
c3	JMP L1	
c4	JMP L1	
c5	JMP L1	56
c6	JMP L1	
c7	JMP L1	
c8	JMP L1	
c9	JMP L2	
c10	JMP L2	
c11	JMP L2	
c12	JMP L2	58
c13	JMP L2	
c14	JMP L2	
c15	JMP L2	
c16	JMP L2	
c17	L1: POP A	
c18	ADD #0	
c19	PUSH A	
c20	RET	
c21	L2: POP A	
c22	ADD #1	
c23	PUSH A	
c24	RET	
d1	DO_REAL: Statements1	Block d
d2	...	
	...	
e1	DO_VOID: Statements2	Block e
e2	...	
	...	

<u>50</u>   Figure 2

Line #	Source Text	Comments
f1	MOV A,V	Block f
f2	MOV DPTR,MASKED_RANDOM	
f3	CALL IRRITATE_2	
f4	JMP DO_REAL	
f5	JMP DO_VOID	
	...	
	...	
g1	IRRITATE_2: JMP @A+DPTR	Block g
g2	...	
	...	
h1	op1	Block h
h2	op2	
h3	op3	
h4	op4	
h5	op5	
h6	op6	
h7	op7	
h8	POP A	
h9	ADD #0	
h10	PUSH A	
h11	RET	
h12	Op1	
h13	Op2	
h14	op3	
h15	op4	
h16	op5	
h17	op6	
h18	op7	
h19	POP A	
h20	ADD #1	
h21	PUSH A	
h22	RET	
i1	DO_REAL: Statements1	Block i
i2	...	
	...	
j1	DO_VOID: Statements2	Block j
j2	...	
	...	

106

108

102

100   Figure 3

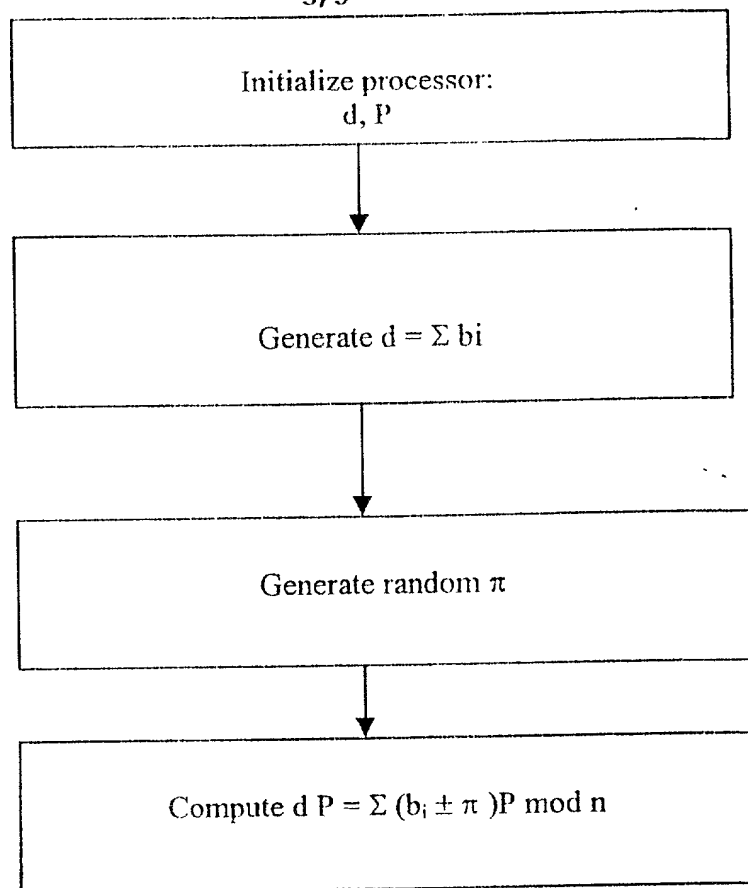| Line #	Source Text	Comments
	MOV A,V JMP IRRITATE_1	*Block k*

	IRRITATE_1: MOV R0,KNOWN_DISPLACEMENT
	SUB R0,THRESHOLD
	MOV DPTR,R0
	JMP @A+DTPR
	...
	...
	JMP DO_VOID
	JMP DO_VOID
	JMP DO_VOID
	JMP DO_VOID
	JMP DO_VOID
	JMP DO_VOID
	JMP DO_VOID
	JMP DO_VOID
	KNOWN_DISPLACEMENT:
	JMP DO_REAL
	JMP DO_REAL
	JMP DO_REAL
	JMP DO_REAL
	JMP DO_REAL
	JMP DO_REAL
	JMP DO_REAL
	JMP DO_REAL
		Block l

	DO_REAL:	*Block m*
	DO_VOID:	*Block n*

Figure 4

Figure 5